Write_Cmd = 0

Idle
SI = Z
CS# = 1
Hold# = 1
WP# = 1
ReadDone = 1
WriteDone = 1
cmdCount = 7
addrCount = 23
dataCount = 7
checkSR = 1

Write_Cmd = 1
Chip Select
CS# = 0
WriteDone = 0

Write_Cmd = 1
Chip Deselect
CS# = 1

Read Status Register
(0x05)
SI = RDSR(cmdCount)

Write_Cmd = 1
Write_Done = 0

Write_Cmd = 1
SR(1) = U
SR(0) = U or 1
CheckSR = 1
SE = 1

dataCount < 0

Read In SR
SR(dataCount) = SO
dataCount --
dataCount >= 0

cmdCount >= 0