MSD II Project Timeline

Week	Dates	Tasks	Task Owners
Break Week 1	Nov 19 - Nov 25		
		Order parts on the BOM for prototype board	Ankit/Zach
		Start working on PCB board layout	Ankit/Zach
		Troubleshoot sampling problems	John/Dan
Break Week 2	Nov 26 -Dec 2		
		Start receiving ordered parts and add them to parts inventory	Ankit/Zach
		Continue working on PCB board layout	Ankit/Zach
		Based on available parts, start the construction of the first analog board prototype	Ankit/Zach
		Continue to troubleshoot sampling problems	John/Dan
Winter 2007 Work Week 1	Dec 3 - Dec 9		
		Continue receiving parts and add them to parts inventory	Ankit/Zach
		Finalize PCB board layout	Ankit/Zach
		Finalize vendor for PCB board manufacturing and update BOM with SMT parts if required	Ankit/Zach
		Continue construction of the first analog board prototype	Ankit/Zach
		Start debugging each stage of the analog board and make component changes as necessary	Ankit/Zach
		Begin implementing visual representation of EEG data	Jonathan
		Continue to troubleshoot sampling problems	John/Dan
		Testing of performance, stability of motes, debugging	John/Dan
Winter 2007 Work Week 2	Dec 10 - Dec 16		
		Ensure all ordered parts have arrived	Ankit/Zach
		Near completion of the first analog board prototype	Ankit/Zach
		Update BOM with SMT parts (if required)	Ankit/Zach
		Hold Design Review to check confirm of PCB layout and to discuss performance of prototype	Ankit/Zach
		Order first PCBs to be manufactured	Ankit/Zach
		Continue implementing visual representation of EEG data	Jonathan
		Continue to troubleshoot sampling problems	John/Dan
		Testing of performance, stability of motes, debugging	John/Dan
Winter 2007 Work Week 3	Dec 17 - Dec 23		
		Complete first analog board prototype	Ankit/Zach
		Start the implementation of the test plan to ensure proper analog board performance	Ankit/Zach
		Ensure all changes made in prototype are reflected in the PCB layout scheme	Ankit/Zach
		Continue implementing visual representation of EEG data	Jonathan
		Test/debug multiplexed sampling of multiple electrodes	John/Dan
		Testing of performance, stability of motes, debugging	John/Dan
Winter Vacation Week 1	Dec 24 - Dec 30		
		Ensure all testing criteria are met for the analog board	Ankit/Zach
		If required, start making a second analog board prototype	Ankit/Zach
		Make arrangements to populate the PCB board with appropriate components (SMT or through hole)	Ankit/Zach

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		Continue implementing visual representation of EEG data	Jonathan
		Test/debug multiplexed sampling of multiple electrodes	John/Dan
		Testing of performance, stability of motes, debugging	John/Dan
		Implement low-power TinyOS code	John/Dan
Winter Vacation Week 2	Dec 31 - Jan 6		
		Continue work on the second analog board prototype if required	Ankit/Zach
		Expect PCBs back from vendor	Ankit/Zach
		Send PCBs to get populated	Ankit/Zach
		Continue implementing visual representation of EEG data	Jonathan
		Testing of mesh-network, performance, stability of motes, debugging	John/Dan
Winter 2007 Work Week 4	Jan 7 - Jan 13		
		Trouble shoot populated PCBs and re-run test plan to ensure proper functionality	Ankit/Zach
		Make changes to the PCB layout/component selection if needed	Ankit/Zach
		Continue implementing visual representation of EEG data	Jonathan
		Testing of mesh-network, performance, stability of motes, debugging	John/Dan
Winter 2007 Work Week 5	Jan 14 - Jan 20		
		Order final edited PCB layout	
		Start working on the enclosure for the Wireless EEG unit	Ankit/Zach
		Build a prototype enclosure using motes and the first set of ordered PCBs	Ankit/Zach
		Continue implementing visual representation of EEG data	Jonathan
		Testing of mesh-network, performance, stability of motes, debugging	John/Dan
Winter 2007 Work Week 6	Jan 21 - Jan 27		
		Finalize the enclosure and wearing scheme of the Wireless EEG unit	Ankit/Zach
		Testing/debugging of real-time plotting of sampled data	Jonathan/John/Dan
Winter 2007 Work Week 7	Jan 28 - Feb 3		
		Receive Final PCB boards	Ankit/Zach
		Send the PCBs to get populated	Ankit/Zach
		Testing/debugging of real-time plotting of sampled data	Jonathan/John/Dan
Winter 2007 Work Week 8	Feb 4 - Feb10		
		Test final populated PCBs for performance by re-implememnting the test plan	Ankit/Zach
		Test sampling, software, networking according to test plan	Jonathan/John/Dan
Winter 2007 Work Week 9	Feb 11 - Feb 17		
		Integrate the analog board and the digital board	Entire Group
		Mount everything inside the designed enclosure	Ankit/Zach
Winter 2007 Work Week 10	Feb 18 - Feb 24		
		Test the system as a whole for required specifications	Entire Group
		Make any last minute changes if required	Ankit/Zach
Winter 2007 Work Week 11	Feb 25 - Feb 29		
		Conduct Final Project Presentation	Entire Group