

Pin	Name	Descrip	Use	Alias	I/O Signals	Physical Location	TP?	Based On Schematic:
1	P4	I/O	Joypad	JOYPAD1_LH	JOYPAD1	LEFT		Rev A7
2	P5	I/O	Joypad	JOYPAD2_LH	JOYPAD2	LEFT		
3	P6	I/O	Joypad	JOYPAD3_LH	JOYPAD3	LEFT		
4	P7	I/O	Joypad	JOYPAD4_LH	JOYPAD4	LEFT		
5	VSS	Chip GND		*	TRIGGER_L	LEFT		
6	BOEn				MOTORCTL_L	LEFT	5	
7	RESn				ACTIONBTN1	RIGHT		
8	VDD	Chip Pwr		*	ACTIONBTN2	RIGHT		
9	P8	I/O	Left Trigger	TRIGGER_LH	TRIGGER_R	RIGHT		
10	P9	I/O	Left Motor PWM	MOTORCTL_LH	MOTORCTL_R	RIGHT	4	
11	P10	I/O	3.3v Standby enable	STBY_EN	PAUSE	LEFT		
					DUTYAUD_L	BOTTOM	6	
12	P11	I/O	12V Enable	VLCD_EN	DUTYAUD_R	BOTTOM	7	
13	P12	I/O	NTSC Dac	*	AUD_SHD	BOTTOM		
14	P13	I/O	NTSC Dac	*	AUD_MUTE	BOTTOM		
15	P14	I/O	NTSC Dac	*	EEPROM_SCL	TOP	19	
16	P15	I/O	Pause Button	PAUSE	EEPROM_SDA	TOP	18	
17	VSS	Chip GND		*	SPI_DO	TOP		
18	VDD	Chip Pwr		*	SPI_CLK	TOP		
19	P16	I/O	Left Duty Mod Audio	DUTYAUD_L	SPI_DI	TOP		
20	P17	I/O	Right Duty Mod Audio	DUTYAUD_R	SPI_CS	TOP		
21	P18	I/O	Audio Amp Standby Control	AUD_SHD	STBY_EN	LEFT	13	
22	P19	I/O	Audio Amp Mute	AUD_MUTE	VLCD_EN	LEFT	11	
					NTSC-1	BOTTOM		
23	P20	I/O	Debug LED	DEBUG1	NTSC-2	BOTTOM		
24	P21	I/O	Debug LED	DEBUG2	NTSC-3	BOTTOM		
25	P22	I/O	Debug LED	DEBUG3	RX	TOP		
26	P23	I/O	Debug LED	DEBUG4	TX	TOP		
27	VSS	Chip GND		*	DEBUG1	RIGHT		
28	XI	OCXO	Oscillator	*	DEBUG2	RIGHT		
29	XO	OCXO	Oscillator	*	DEBUG3	RIGHT		
30	VDD	Chip Pwr			DEBUG4	RIGHT		
31	P24	I/O	Right Motor Control	MOTORCTL_RH				
32	P25	I/O	ActionButtons	ACTIONBTN2_RH	Total		32	
33	P26	I/O	Action Buttons	ACTIONBTN1_RH	Available		32	
34	P27	I/O	Right Trigger	TRIGGER_RH				
35	P28	I/O	EEPROM I2C Clk	EEPROM_SCL				

36	P29	I/O	EEPROM I2C Data	EEPROM_SDA						
37	P30	I/O	Programming Receive232	RX						
38	P31	I/O	Programming Transmit232	TX						
39	VSS	Chip GND		*						
40	VDD	Chip Pwr		*						
41	P0	I/O	SD Data Out (Card -> Host)	SPI_DO						
42	P1	I/O	SD CLK Source	SPI_CLK						
43	P2	I/O	SD Chip Select	SPI_CS						
44	P3	I/O	SD Data In (Host -> Card)	SPI_DI						