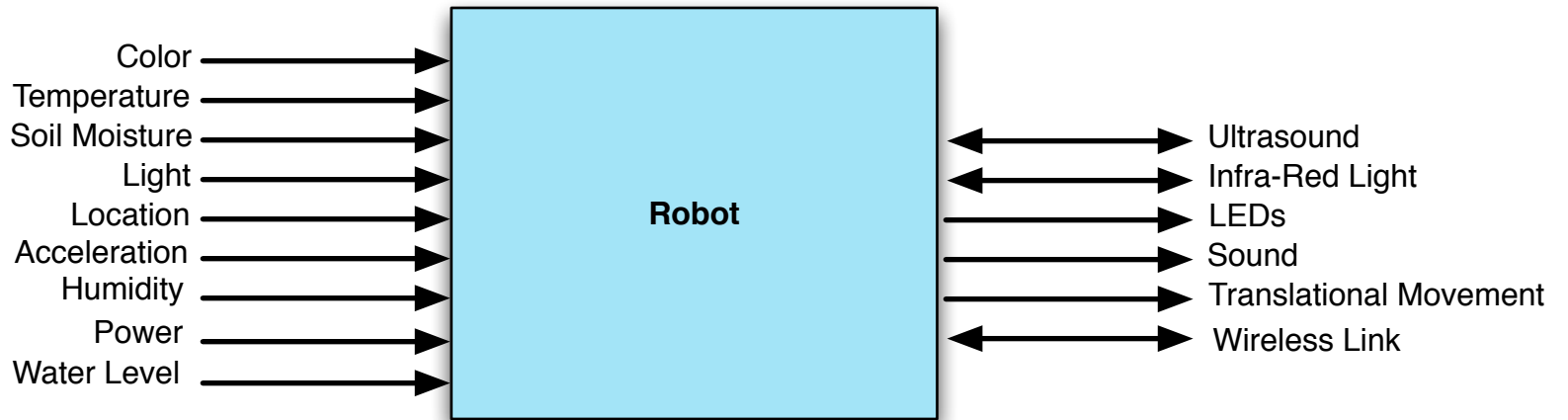


Level 0: Input/Output Diagram



Level 1: Platform Architecture

