

Team: 18262 Engineer: Murali Prasad

Entering Phase: Detailed Design Review

What did I plan to do for the detailed design review, and what did I get done?

1. Configure the SoC chip for small and large scale tests
2. Run small scale test and record data
3. Run large scale test and record data
4. I wasn't able to complete a full test due to complications with the communication to the chip.

What do I plan on doing to ensure that Team P18262 has a successful Phase (MSD II) review?

1. Acquire an EV2300 module to help with configuring the chip
2. Run both tests successfully

You will answer these three questions when you submit at the end of Phase (MSD II).

1. Did the SoC chip tests work?
2. What is needed to be done in firmware to make it work on the BMS?