

Team: 18262 _____ **Engineer: Murali Prasad** _____

Entering Phase: Integrated System Build and Test

What did I do/learn in Phase (subsystem build and test) review?

1. I updated the SoC test plans to include tests for small and large scale
2. I configured the chip to work for the 4s (16V) pack
3. I ran a discharge test with the 4s pack and collected data
4. I learned that there are many steps in configuring the chip including setting flash values and running a training cycle

What do I plan on doing to ensure that Team P18262 has a successful Phase (integrated system build and test) review?

1. Validate proper functionality of chip with 4s pack after discharge cycle
2. Configure chip for 50V pack
3. Complete large scale test with 50V pack and collect data

You will answer these three questions when you submit at the end of Phase (integration system build and test).

1. What did I actually do?
2. What did I learn?
3. What do I plan on doing to ensure that Team P1862 has a successful Phase (customer handoff) review?