

**Team: 18262\_\_\_\_\_ Engineer: Jacob Allison**

**Entering Phase: Detailed Design Review**

**What was did I plan for the system design review, and what did I get done?**

- 1. All work was completed on time, and some of the work done**
- 2. Verified functionality of LTC6811 Chip.**
  - a. All functionalities of the chip work as intended.**
- 3. Tested LTC DC2260A Demo Board.**
  - a. Found and utilized working libraries for LTC6811.**
- 4. Created and executed test plans for the demo boards to be used on the slave boards.**
  - a. Sending configuration to LTC6811**
    - i. Addressable and broadcast.**
  - b. Reading configuration from LTC6811**
  - c. Cell voltage measurement**
  - d. Cell balancing**
  - e. GPIO voltage measurement**

**What do I plan on doing to ensure that Team P18262 has a successful Phase (Detailed Design) review?**

- 1. Review schematics for the slave board and make sure there are no mistakes or if any improvements can be made.**
- 2. Check what tests can be made on the slave boards and create test plans for anything that may need to be tested.**
  - a. This will include input voltage ranges, output voltages, cell balancing, and overall functionality and ability to function under different circumstances,**
- 3. Create test plan for getting the LTC6811 working as intended.**
  - a. This will utilize similar tests to the Demo Board.**
  - b. These tests will need to be done after hardware validation.**

**You will answer these three questions when you submit at the end of Phase (Detailed Design Review).**

- 1. Was all work done on time?**
- 2. Were all schematics and components of the slave board checked for mistakes or improvements?**
  - a. If there are any changes that need made, what are they?**
- 3. What tests were developed? Are there any tests that we want to run but cannot?**
- 4. Does the LTC6811 chip work as intended?**