

**Team: 18262      Engineer: Murali Prasad**

**Entering Phase: Detailed Design Review**

**What did I plan to do for the system design review, and what did I get done?**

1. The chip schematic with support circuitry on Altium is done
2. The functional test wasn't performed as expected due to switching chips and difficulties communicating to chip for configuration
3. The chip was able to be mostly configured for a small test setup
4. Multiple development boards were received to use for testing
5. Faculty members were able to look at some schematics and give feedback for how to proceed carefully
6. Created a 3-D Model to see what boards would look like with connectors and check size constraints

**What do I plan on doing to ensure that Team P18262 has a successful Phase (Detailed Design) review?**

1. Verify chip configurations for both tests
2. Complete tests for chosen TI chip on small scale (low voltage pack in range of chip) and large scale (higher voltage which needs to be scaled internally)
3. Create more detailed 3-D model and communicate with Battery Box team to make sure they would fit

**You will answer these three questions when you submit at the end of Phase (Detailed Design Review).**

1. Did the test show the chip will work?
2. Do the connector placements work with the space available?
3. Would an Algorithm or the Chip work better?