

Team: 18262 _____

Engineer: Steve Titus

Entering Phase: Detailed Design Review

What did I plan for the system design review, and what did I get done?

- 1. I planned to do the schematics for the MCU and supporting circuitry. I finished the schematics for the MCU and supporting circuitry. The circuitry has been reviewed by EVT, the MSD team, and at the MSD design review, professor Carlos Barrios.**
- 2. I supported Ben in testing the SPI communication**
- 3. The team did not stay on schedule in terms of temperature testing and SOC testing. I focused my time on my work as opposed to checking in with my group members. To prevent this from happening again, I scheduled a mid term review for next phase and am reviewing everyone's 3 week plans to make sure they understand their roles and responsibilities.**

What do I plan on doing to ensure that Team P18262 has a successful Phase (Detailed Design) review?

- 1. I plan on creating a firmware test plan. This means a test plan for the hardware that requires firmware. An example would be an oscilloscope capture to make sure waveforms look good.**
- 2. I plan on creating a firmware block diagram for hardware validation code.**
- 3. I plan on writing I2C drivers.**

You will answer these three questions when you submit at the end of Phase (Detailed Design Review).

- 1. Did the team finish on schedule?**
- 2. Are the test plan and block diagram done?**
- 3. Are the drivers written?**