






















ID	WBS	Task Name	Duration	Start	Finish	Predecessors	Oct 27, '17							Sep 3, '17						
							M	T	W	T	F	S	S	M	T	W				
1		Group Specific Deliverables																		
2	G.1	Automated Test Fixture	178 days	Tue 8/29/17	Thu 5/3/18															
3	G.10	Phase III: Subsystem Design	21 days	Tue 10/17/17	Tue 11/14/17															
4	8	Week 8	5 days	Tue 10/17/17	Mon 10/23/17															
5	8.1	Define Calibration Subsystem	1 day	Tue 10/17/17	Tue 10/17/17															
6	8.2	Research MCU options + technical specs	1 day	Tue 10/17/17	Tue 10/17/17															
7	8.3	OCWR Testing	1 day	Tue 10/17/17	Tue 10/17/17															
8	8.4	Transceiver Evaluation	1 day	Tue 10/17/17	Tue 10/17/17															
9	8.5	Identify risks + Mitigation Plan	1 day	Wed 10/18/17	Wed 10/18/17															
10	8.6	Update Function Decomposition	1 day	Thu 10/19/17	Thu 10/19/17															
11	8.7	Create Gantt Chart (Phase II + Phase IV)	1 day	Thu 10/19/17	Thu 10/19/17															
12	8.8	Create Optical Power Budget	3 days	Thu 10/19/17	Mon 10/23/17															
13	8.9	Request Transceivers	1 day	Thu 10/19/17	Thu 10/19/17															
14	8.10	Request STEP File (Bud Industries)	3 days	Fri 10/20/17	Tue 10/24/17															
15	9	Week 9	5 days	Tue 10/24/17	Mon 10/30/17															
16	9.1	Update Pugh Chart	1 day	Tue 10/24/17	Tue 10/24/17															
17	9.2	Update Function Diagram	1 day	Tue 10/24/17	Tue 10/24/17															
18	9.3	Aurdiono SW Research	1 day	Tue 10/24/17	Tue 10/24/17															
19	9.4	Create Working Document for Mitigation Plan	1 day	Tue 10/24/17	Tue 10/24/17															
20	9.5	Buy MCU & Touchscreen	4 days	Thu 10/19/17	Tue 10/24/17															
21	9.6	Power Management Block Diagram	1 day	Tue 10/24/17	Tue 10/24/17															
22	9.7	Optical Path Drawing	1 day	Tue 10/24/17	Tue 10/24/17															
23	9.8	Design Optical Setup	1 day	Tue 10/24/17	Tue 10/24/17															

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24	9.9	Identify mechanical requirements for CAD Model	2 days	Sat 10/21/17	Mon 10/23/17															
25	9.10	Serial Communication Function Research	3 days	Tue 10/24/17	Thu 10/26/17															
26	9.11	Collect Return Loss Measurement Data	3 days	Tue 10/24/17	Thu 10/26/17															
27	9.12	Finalize Budget Plan	1 day	Tue 10/24/17	Tue 10/24/17															
28	9.13	Update EDGE	1 day	Thu 10/26/17	Thu 10/26/17															
29	10	Week 10	5 days	Tue 10/31/17	Mon 11/6/17															
30	10.1	Begin Transceiver per Board Laser Prototyping	4 days	Thu 10/26/17	Tue 10/31/17															
31	10.2	Preliminary Power Circuitry	1 day	Tue 10/31/17	Tue 10/31/17															
32	10.3	Design Signal Conditioning Circuit	1 day	Tue 10/31/17	Tue 10/31/17															
33	10.4	Brainstorm Lean Implementation Plan	3 days	Tue 10/31/17	Thu 11/2/17															
34	10.5	Mitigation Plan	1 day	Tue 10/31/17	Tue 10/31/17															
35	10.6	Engineering Requirement Edits	5 days	Thu 10/26/17	Thu 11/2/17															
36	10.7	Touchscreen Pseudo Code	3 days	Tue 10/31/17	Thu 11/2/17															
37	10.8	Continue Preliminary Testing	5 days	Tue 10/31/17	Mon 11/6/17															
38	10.9	Begin 3D Model Draft	0 days	Sun 11/5/17	Sun 11/5/17															
39	11	Week 11	5 days	Tue 11/7/17	Mon 11/13/17															
40	11.1	Touchscreen Pseudo Code	3 days	Sat 11/4/17	Tue 11/7/17															
41	11.2	User Test the UI	3 days	Sat 11/4/17	Tue 11/7/17															
42	11.3	Redesign Optical Path	4 days	Thu 11/2/17	Tue 11/7/17															
43	11.4	Recalculate Optical Power Budget	4 days	Thu 11/2/17	Tue 11/7/17															
44	11.5	Edit 3D Model	2 days	Tue 11/7/17	Wed 11/8/17															

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45	11.6	Request SM/MM Ports	1 day	Tue 11/7/17	Tue 11/7/17															
46	11.7	Complete EDGE Control for Phase III	3 days	Tue 11/7/17	Thu 11/9/17															
47	11.8	Printer Purchase Request	4 days	Thu 11/2/17	Tue 11/7/17															
48	11.9	Signal Conditioning Circuit Diagrams	4 days	Thu 11/2/17	Tue 11/7/17															
49	11.10	Power Circuitry	4 days	Thu 11/2/17	Tue 11/7/17															
50	11.11	Three Week Plan	2 days	Wed 11/8/17	Thu 11/9/17															
51	11.12	Prepare for Subsystem Design Review	1 day	Tue 11/7/17	Tue 11/7/17															
52	11.13	Subsystem Design Review	0 days	Thu 11/9/17	Thu 11/9/17	51														
53	11.14	Peer Evaluation	2 days	Thu 11/9/17	Fri 11/10/17															
54	G.11	Phase IV: Complete Detailed Design	24 days	Thu 11/9/17	Wed 12/13/17															
55	12	Week 12	5 days	Tue 11/14/17	Mon 11/20/17															
56	12.1	Finalize BOM	1 day	Tue 11/14/17	Tue 11/14/17															
57	12.2	Finalize Purchases	1 day	Tue 11/14/17	Tue 11/14/17															
58	12.3	Work on Detailed Pspice	3 days	Tue 11/14/17	Thu 11/16/17															
59	12.4	Continue Developing Lean Implementation Plan	3 days	Tue 11/14/17	Thu 11/16/17															
60	12.5	Finalize Purchases	4 days	Thu 11/9/17	Tue 11/14/17															
61	12.6	Continue 3D CAD Model	5 days	Sat 11/11/17	Thu 11/16/17															
62	12.7	Update EDGE	1 day	Thu 11/16/17	Thu 11/16/17															
63	12.8	Prototype Signal Conditioning Circuit	1 day	Thu 11/16/17	Thu 11/16/17															
64	12.9	Begin MSD II Plan (Draft of Paper)	5 days	Tue 11/14/17	Mon 11/20/17															
65	13	Week 13	5 days	Tue 11/21/17	Mon 11/27/17															
66	13.1	Test Engineering Requirements with POTATO System	3 days	Sat 11/18/17	Tue 11/21/17															

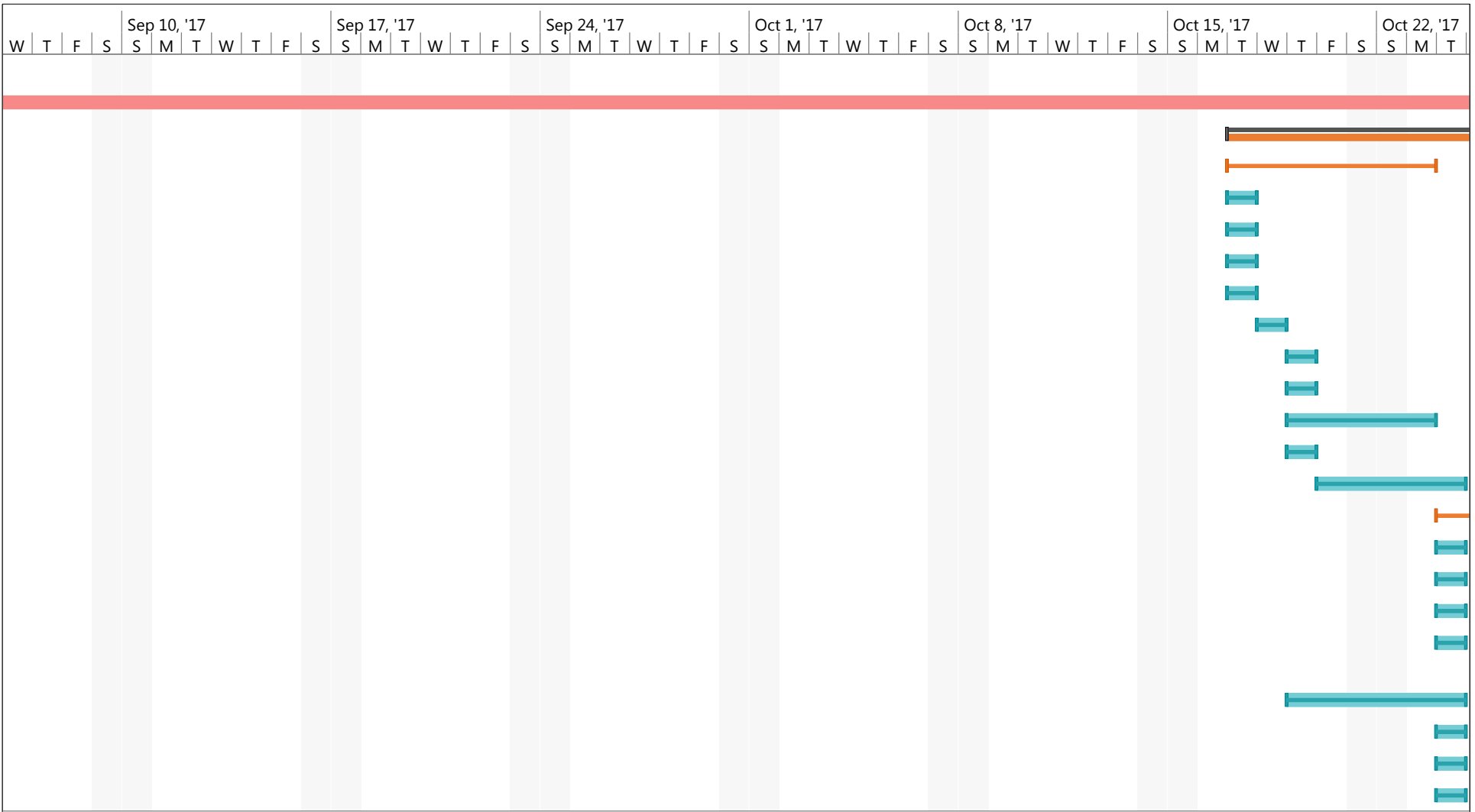
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67	13.2	Finalize 3D Draft	3 days	Sat 11/18/17	Tue 11/21/17															
68	13.3	Review Risks (High Risk)	1 day	Tue 11/21/17	Tue 11/21/17															
69	13.4	Update Documentation to EDGE	1 day	Thu 11/23/17	Thu 11/23/17															
70	13.5	Continue MSD II Plan (Draft of Paper)	3 days	Tue 11/21/17	Thu 11/23/17															
71	14	Week 14	5 days	Tue 11/28/17	Mon 12/4/17															
72	14.1	Improve Implementation Plan	3 days	Sat 11/25/17	Tue 11/28/17															
73	14.2	Continue MSD II Plan (Draft of Paper)	7 days	Sat 11/25/17	Mon 12/4/17															
74	14.3	Update Documentation to EDGE	1 day	Thu 11/30/17	Thu 11/30/17															
75	15	Week 15	5 days	Tue 12/5/17	Mon 12/11/17															
76	15.1	Finish MSD II Plan (Draft of Paper)	5 days	Sat 12/2/17	Thu 12/7/17															
77	15.2	Update EDGE	1 day	Tue 12/5/17	Tue 12/5/17															
78	15.3	Prepare for Gate Review (Complete Detailed Design)	1 day	Tue 12/12/17	Tue 12/12/17															
79	G.2	Gate Review (Complete Detailed Design)	0 days	Wed 12/13/17	Wed 12/13/17	78														

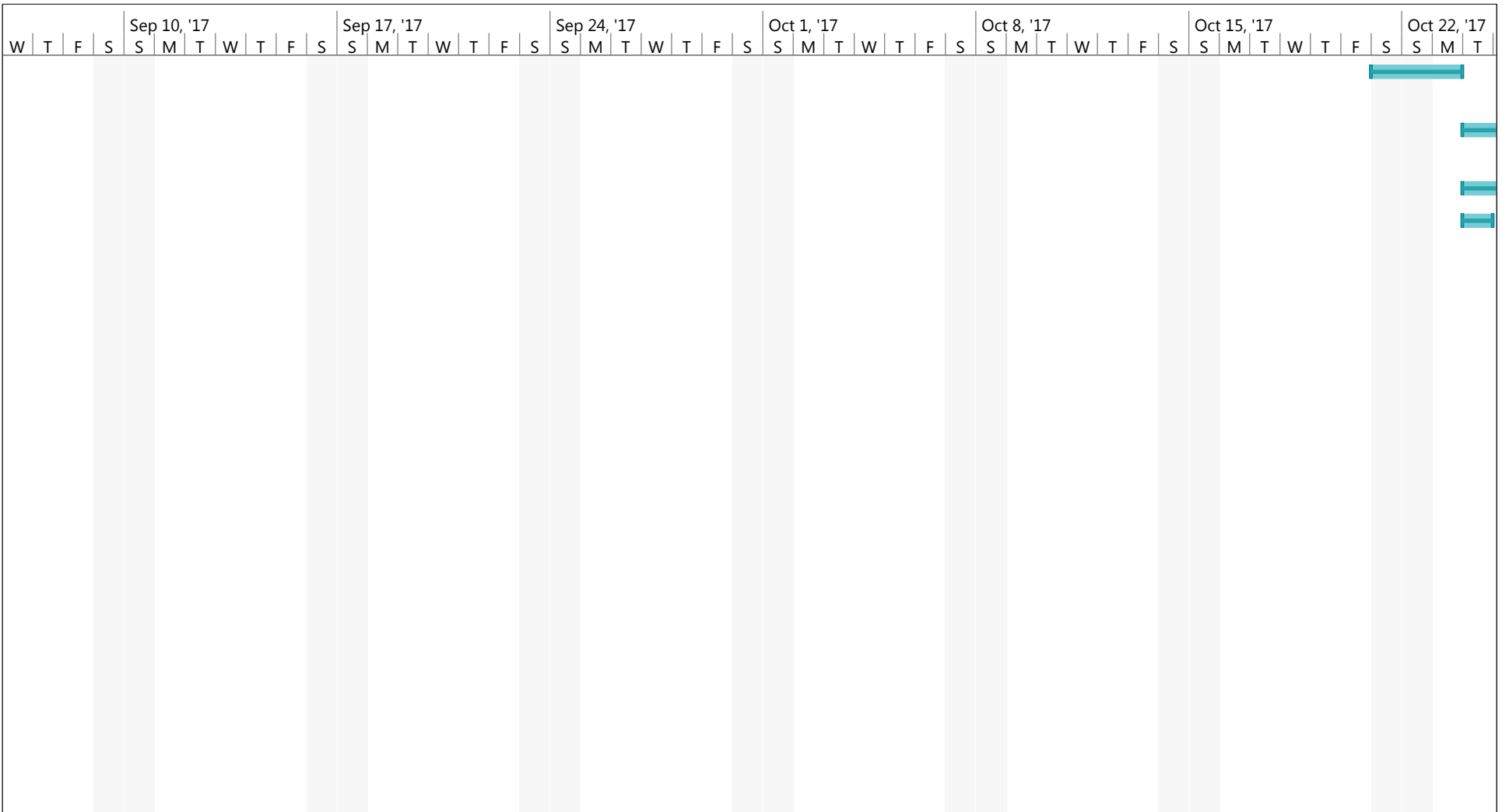
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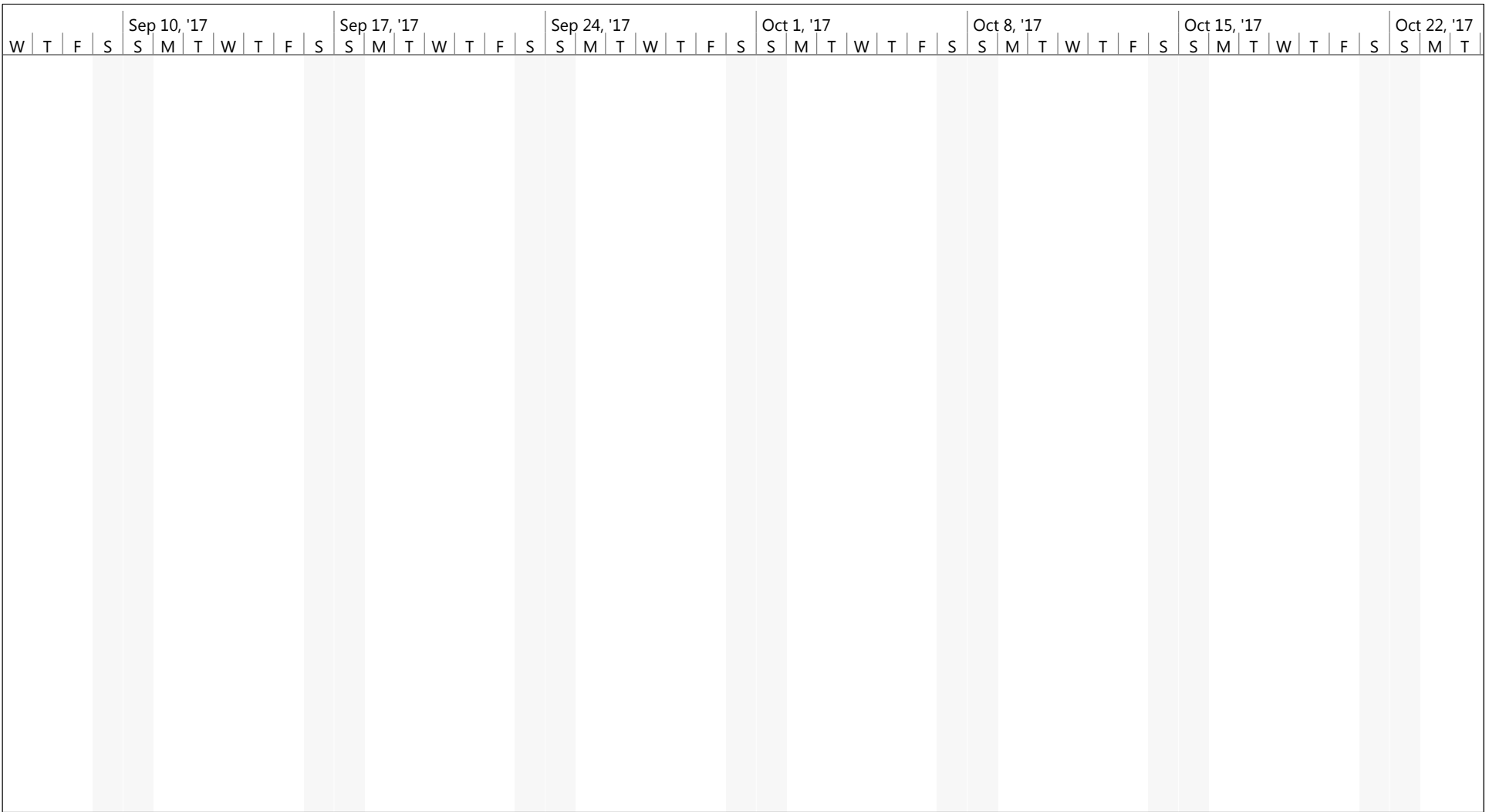
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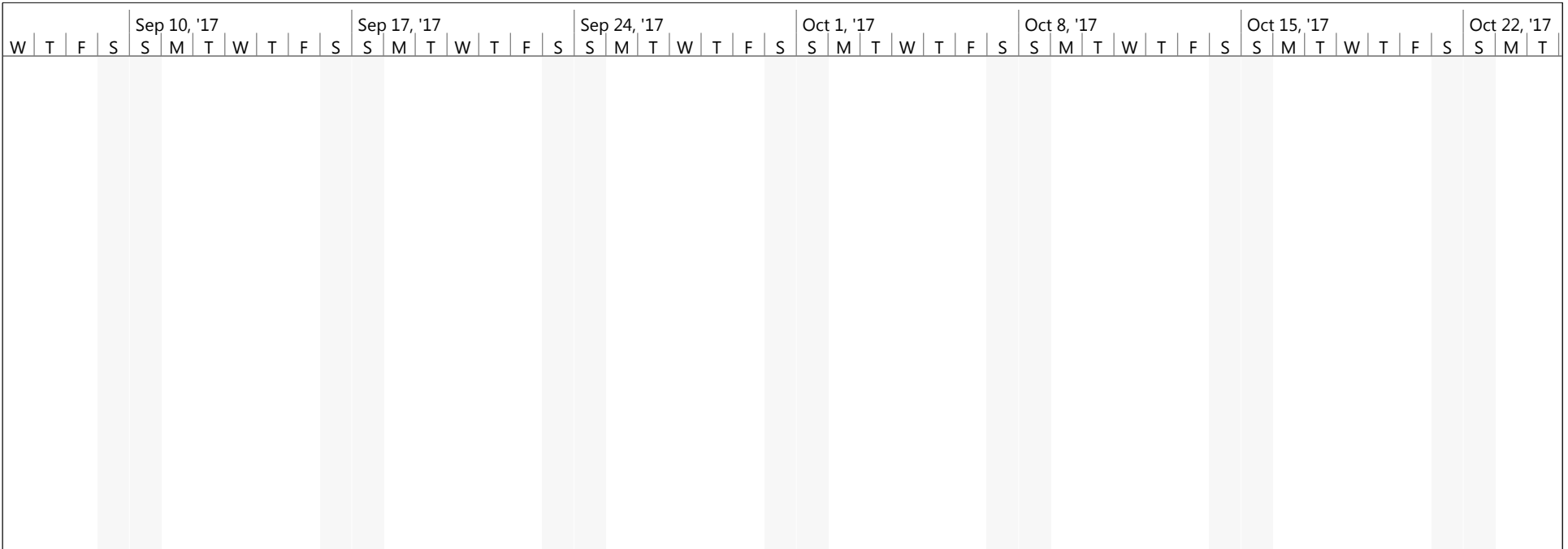
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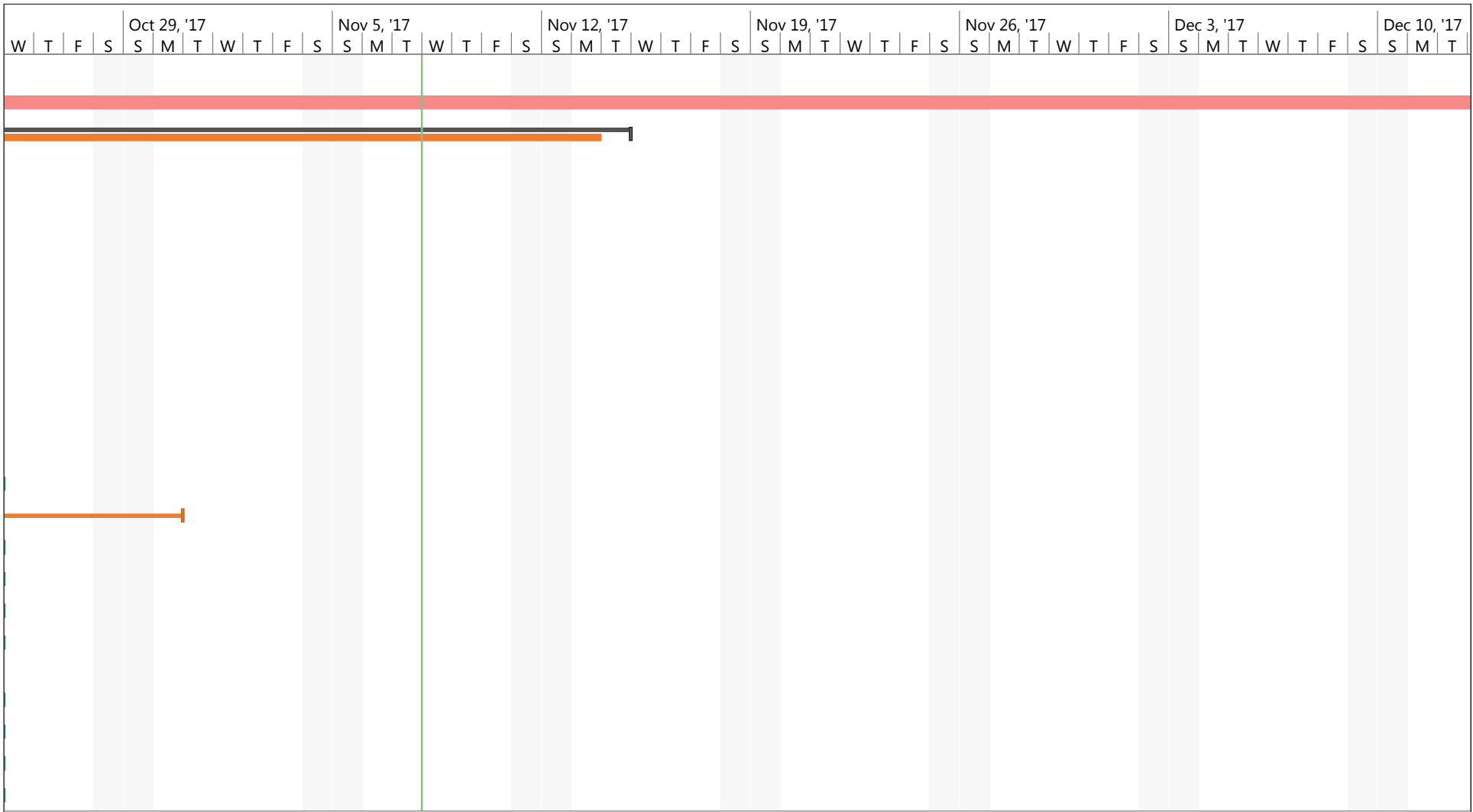
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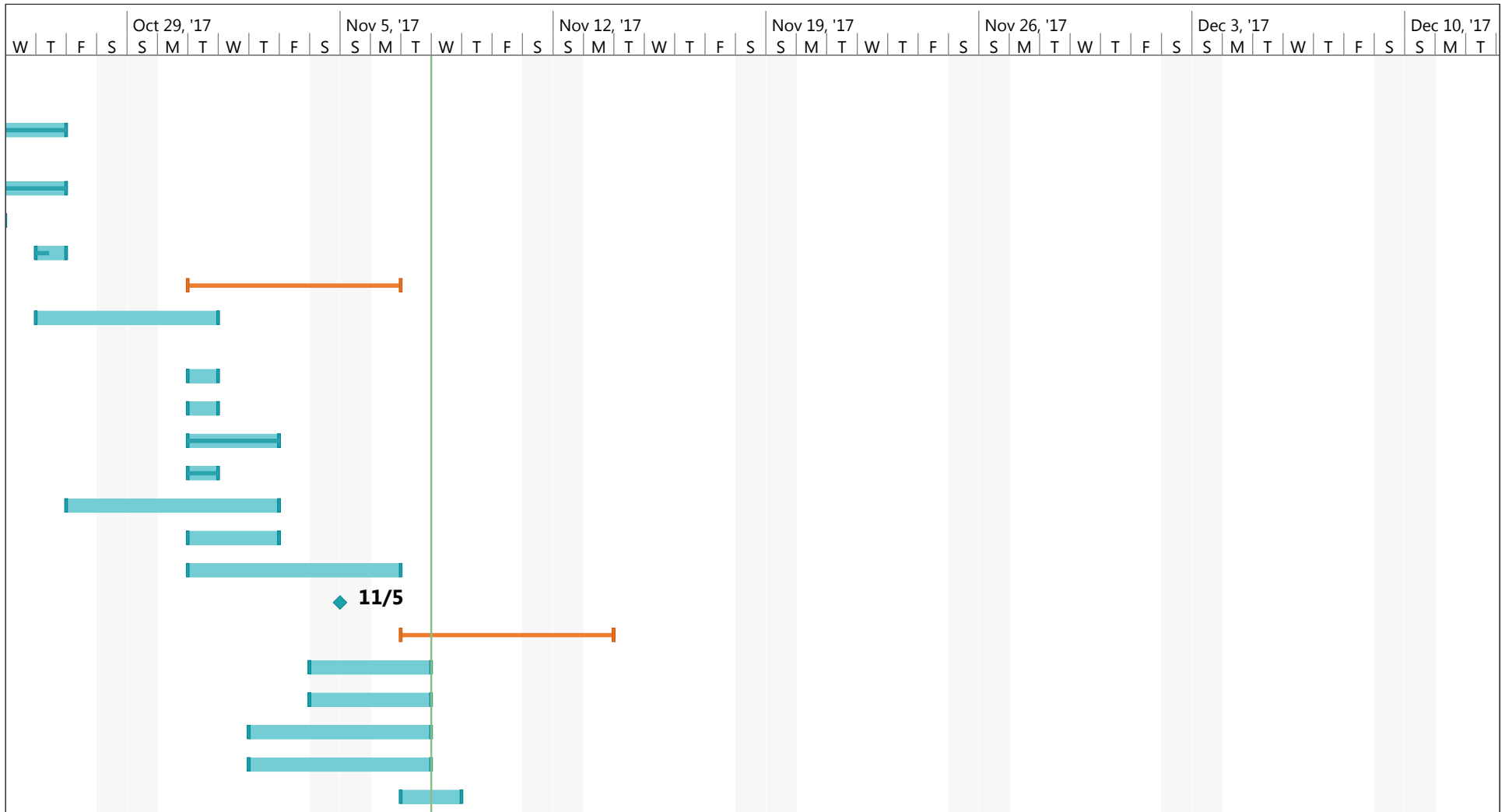
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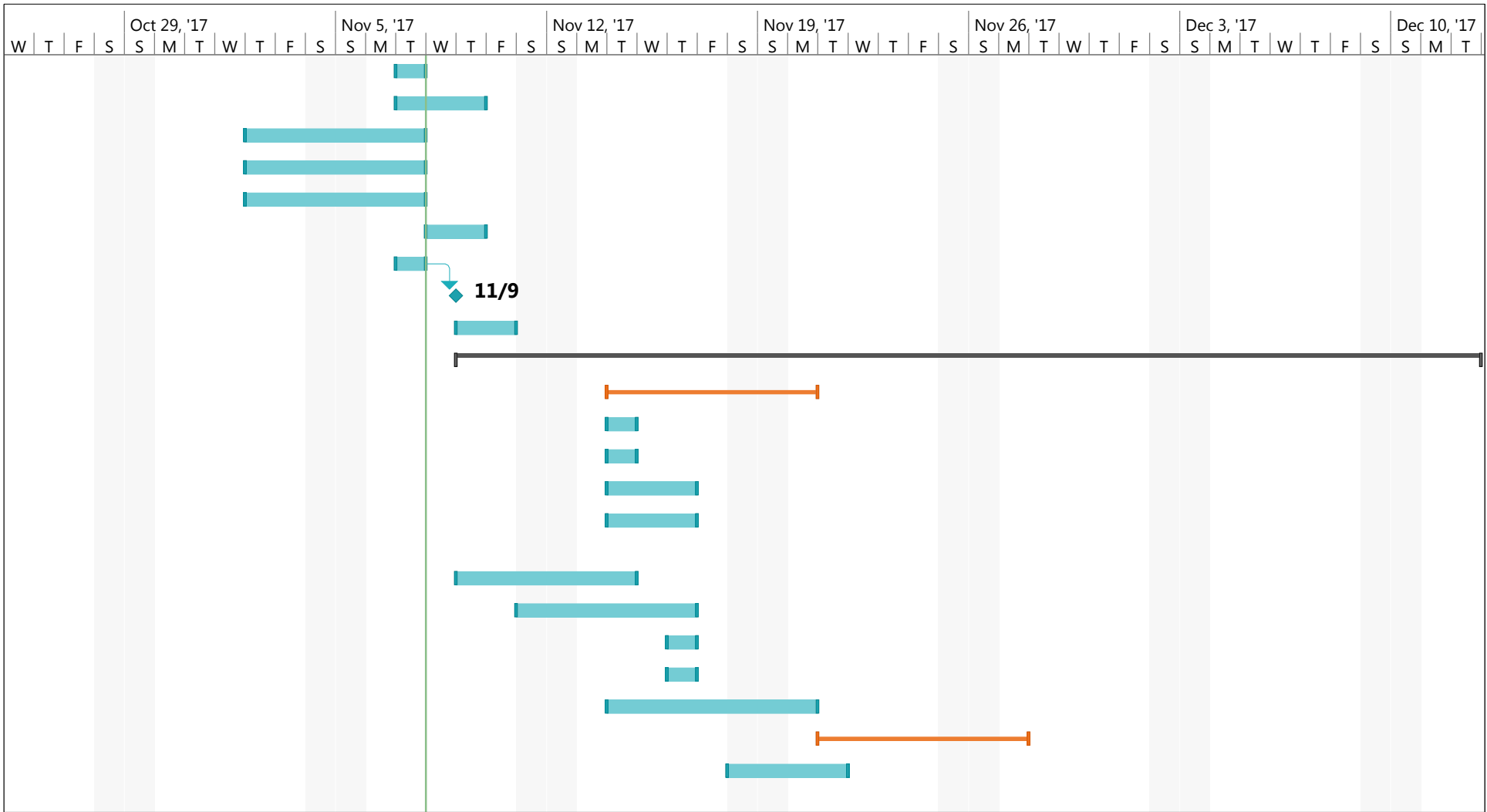
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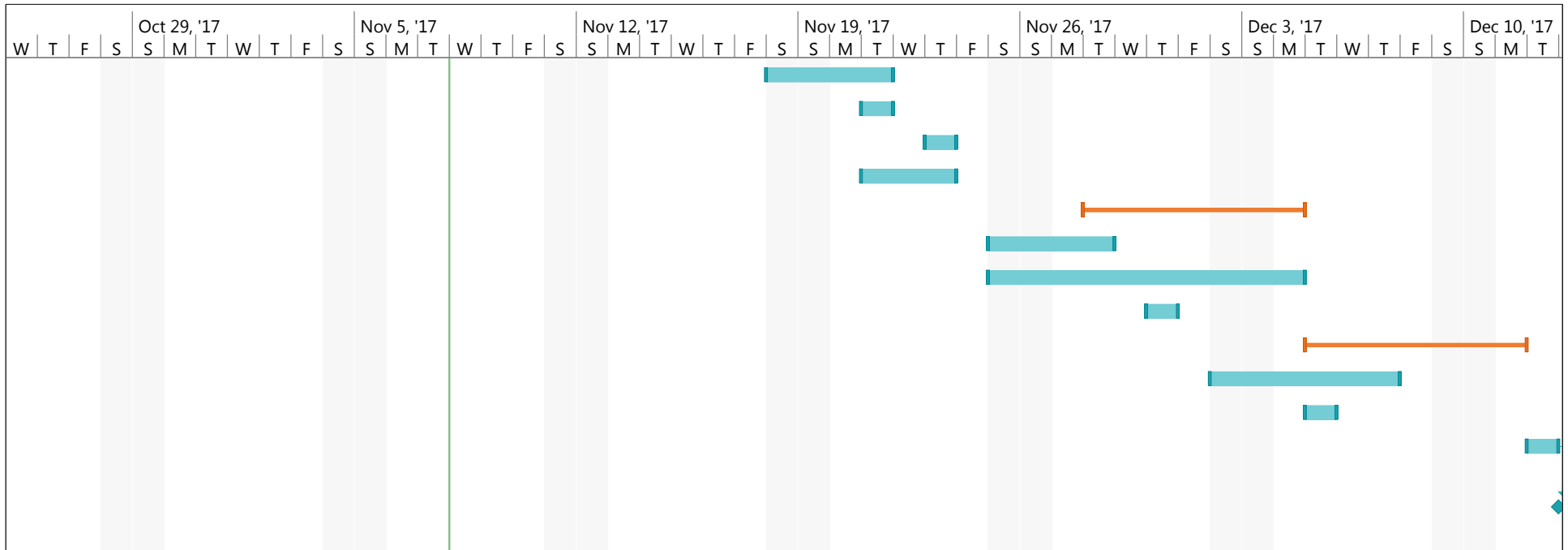
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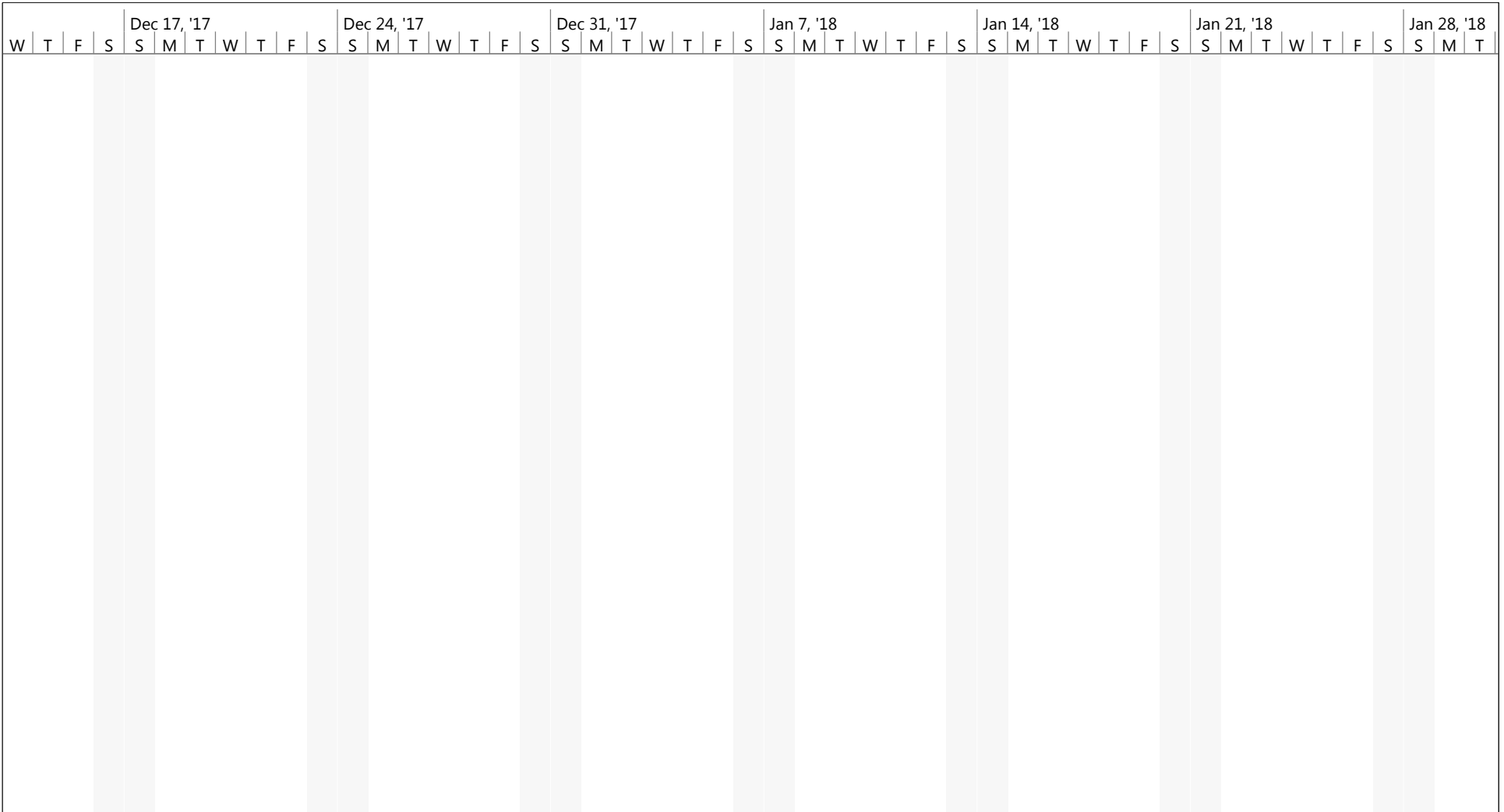
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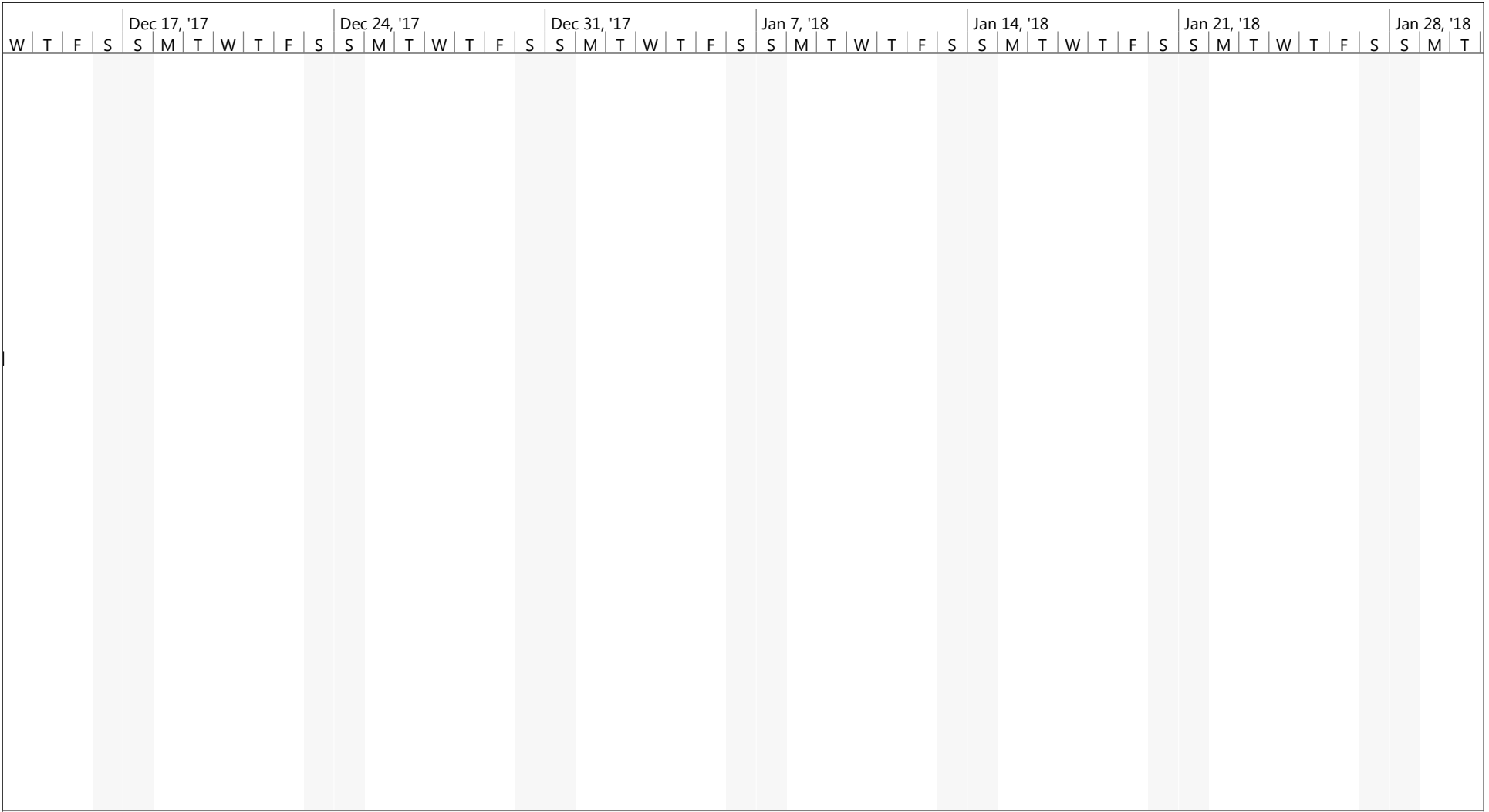
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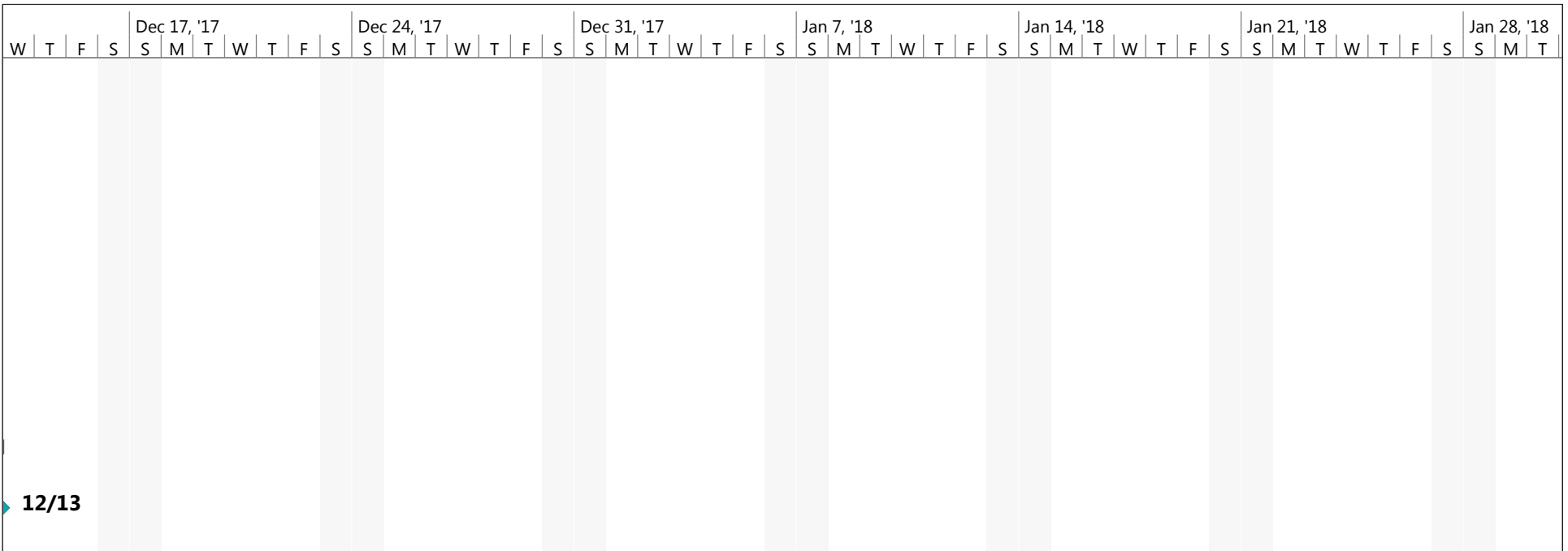
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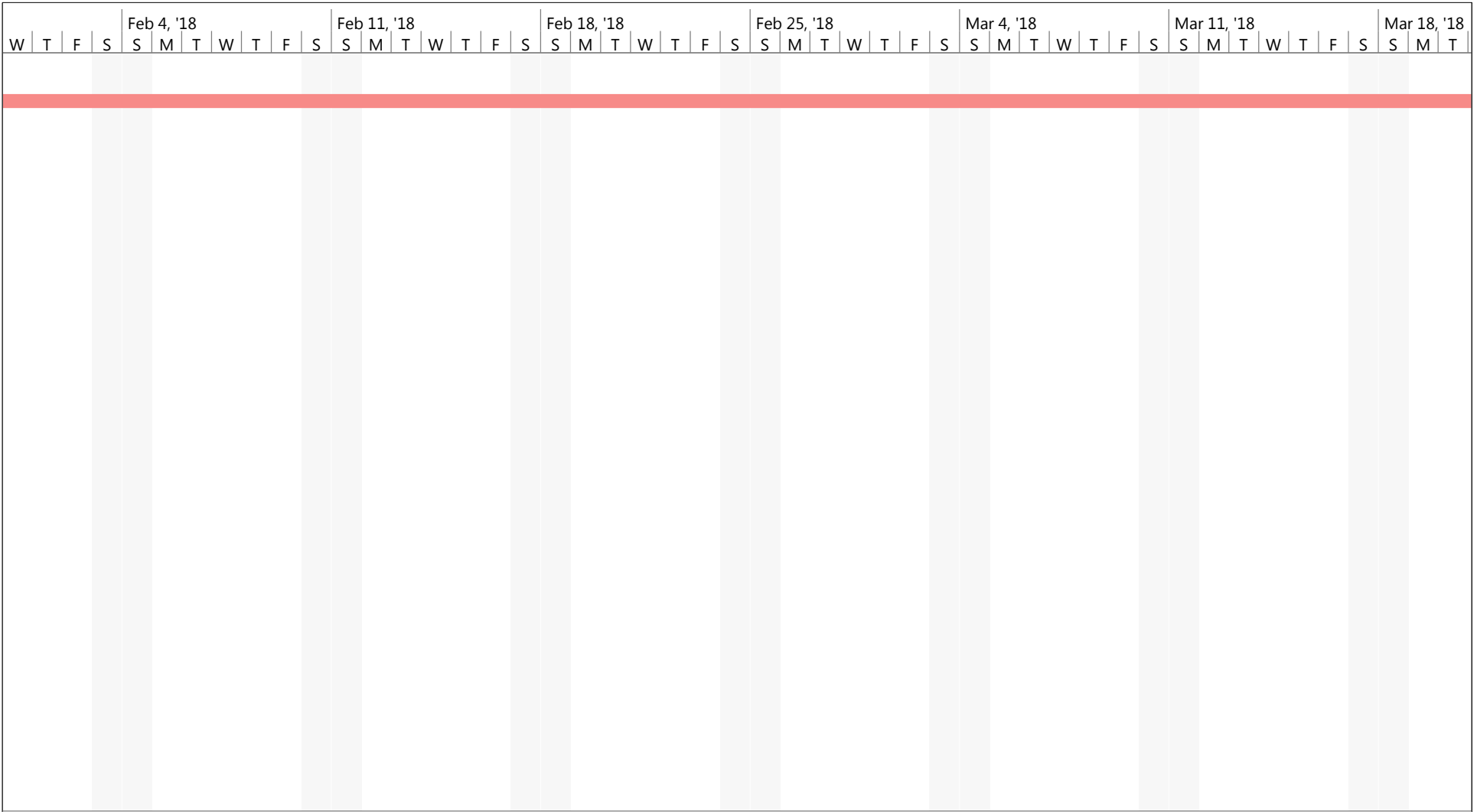


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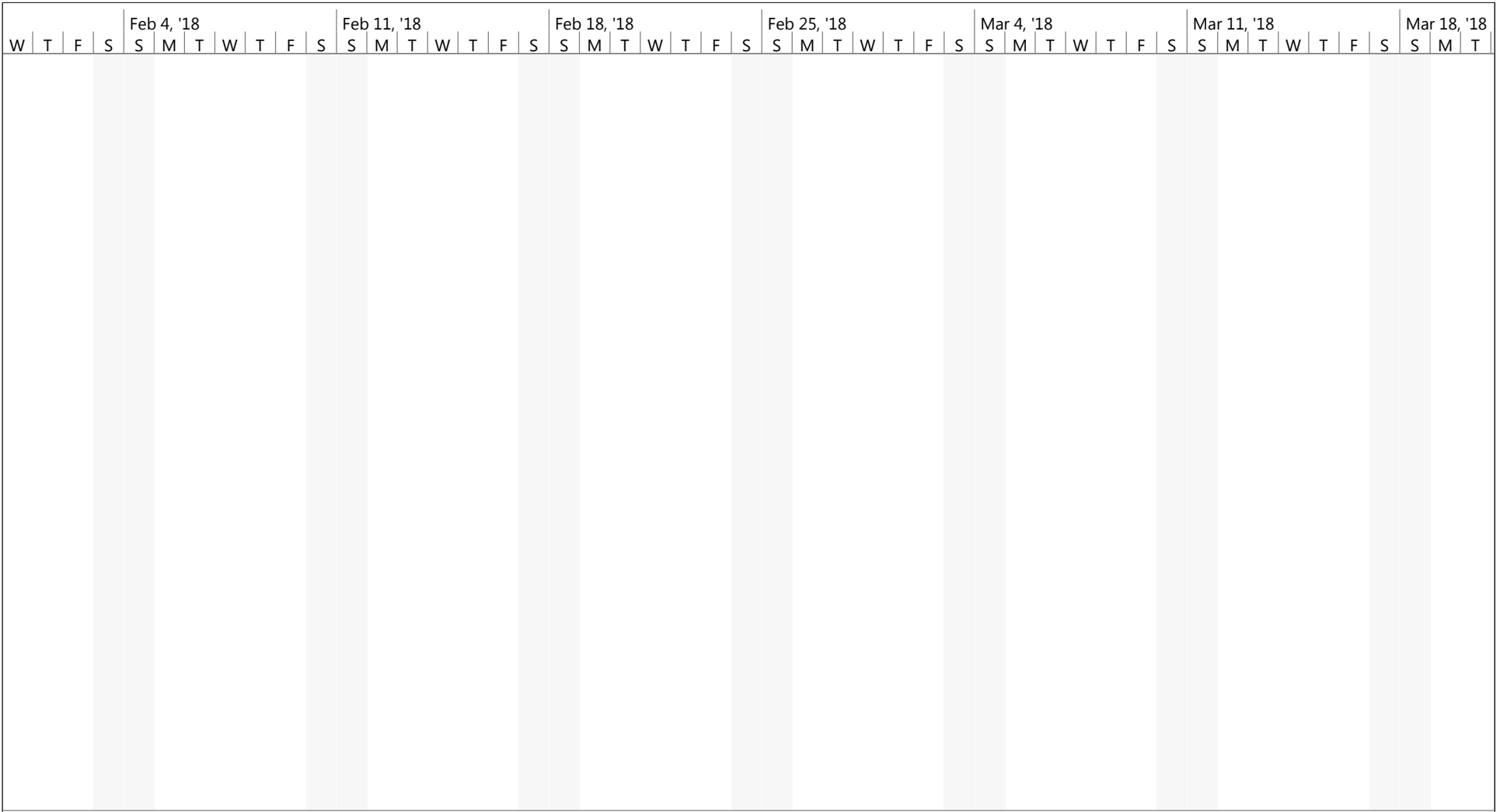


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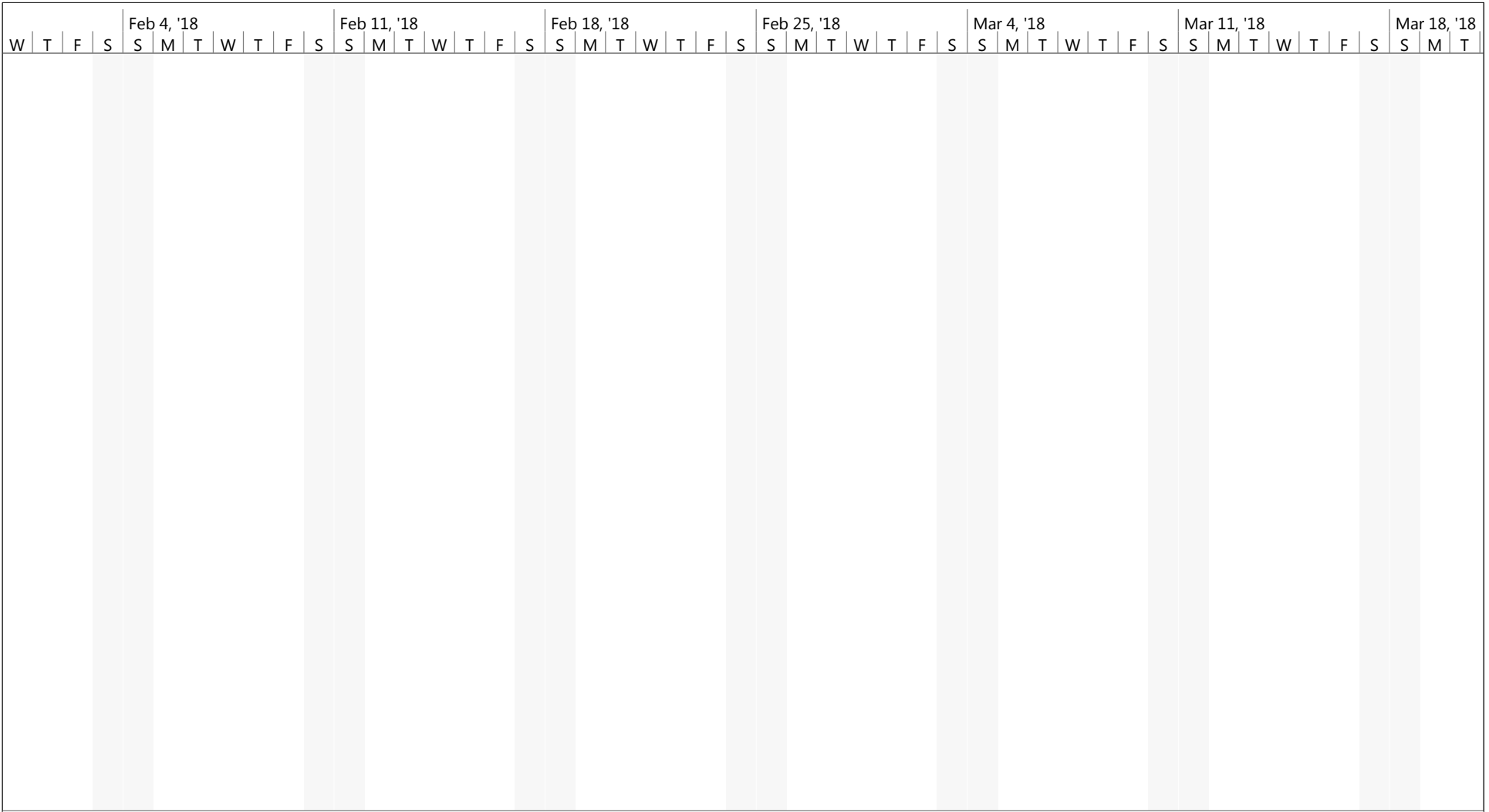
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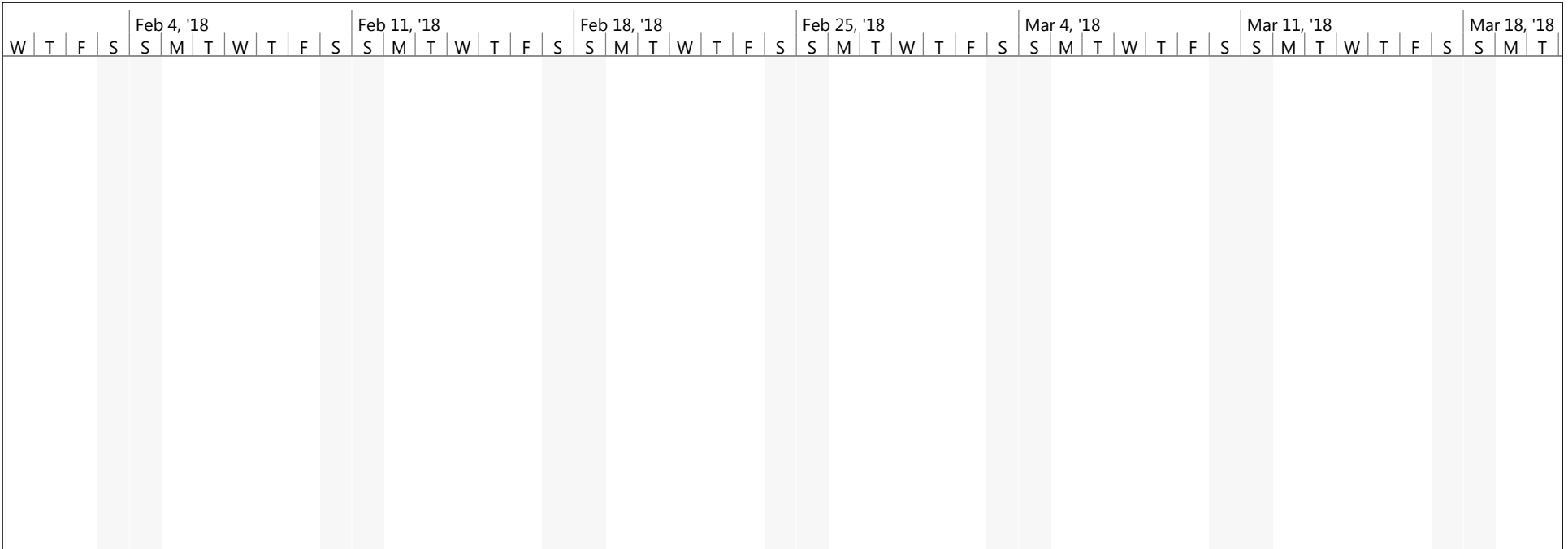
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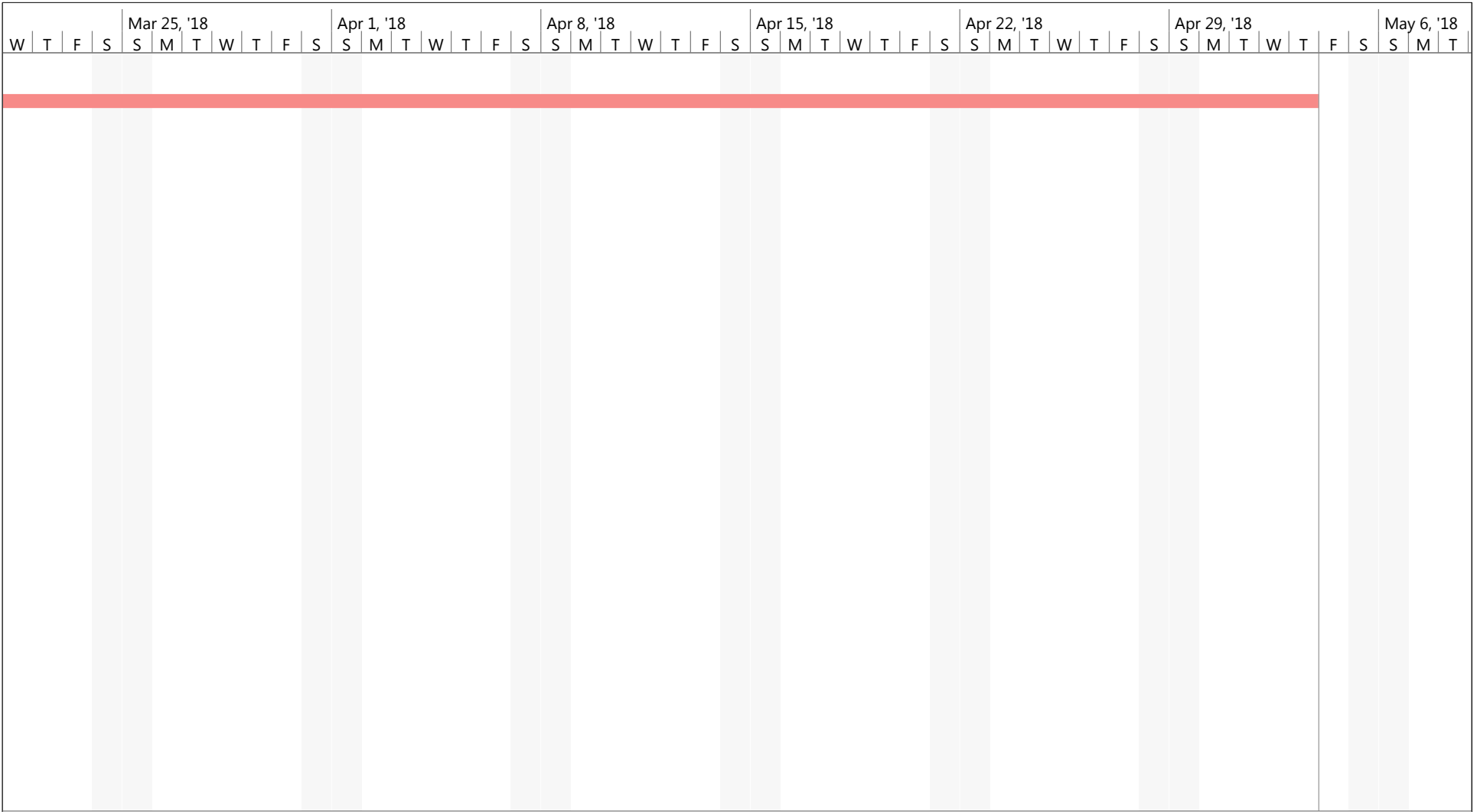
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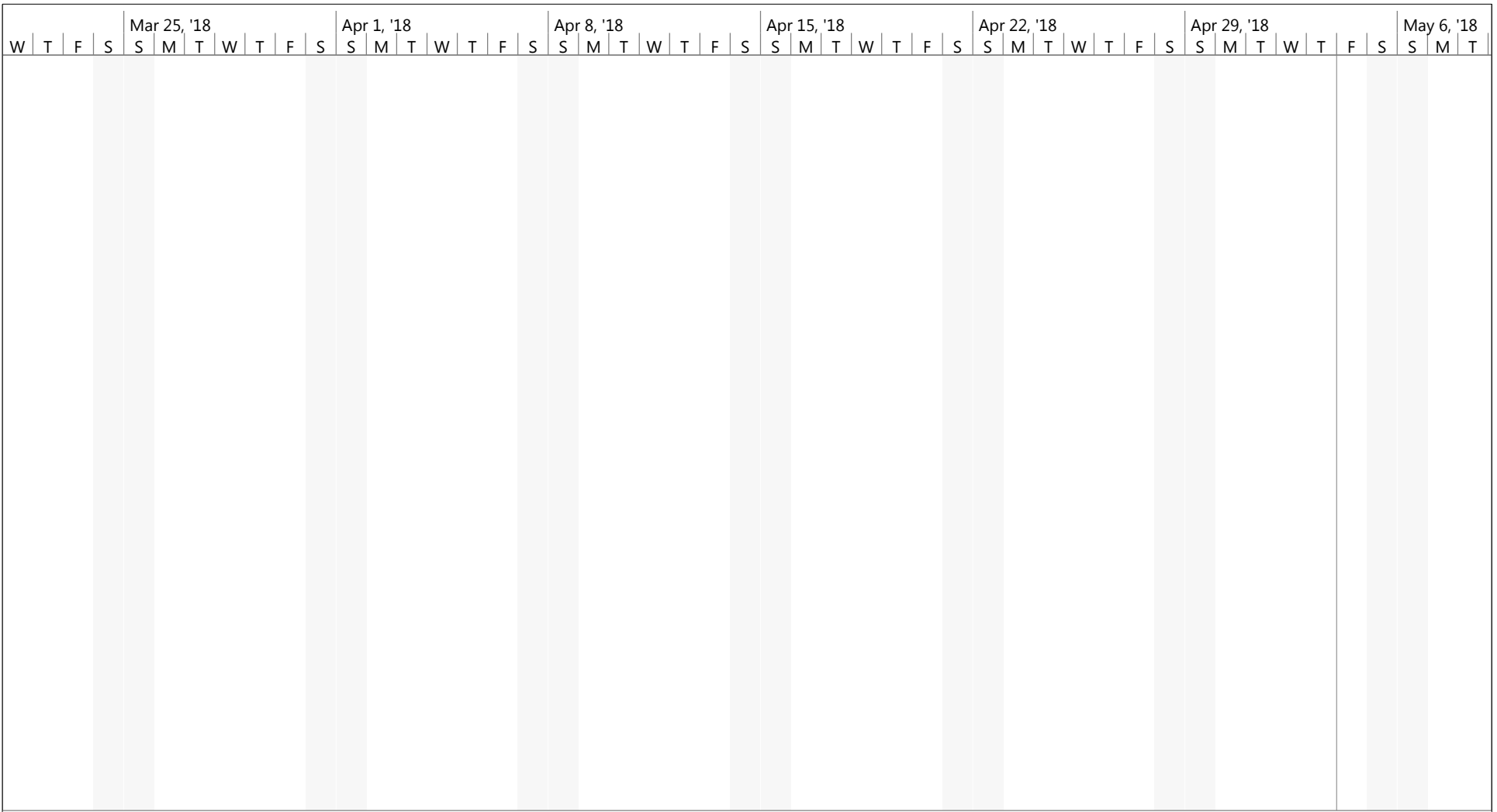
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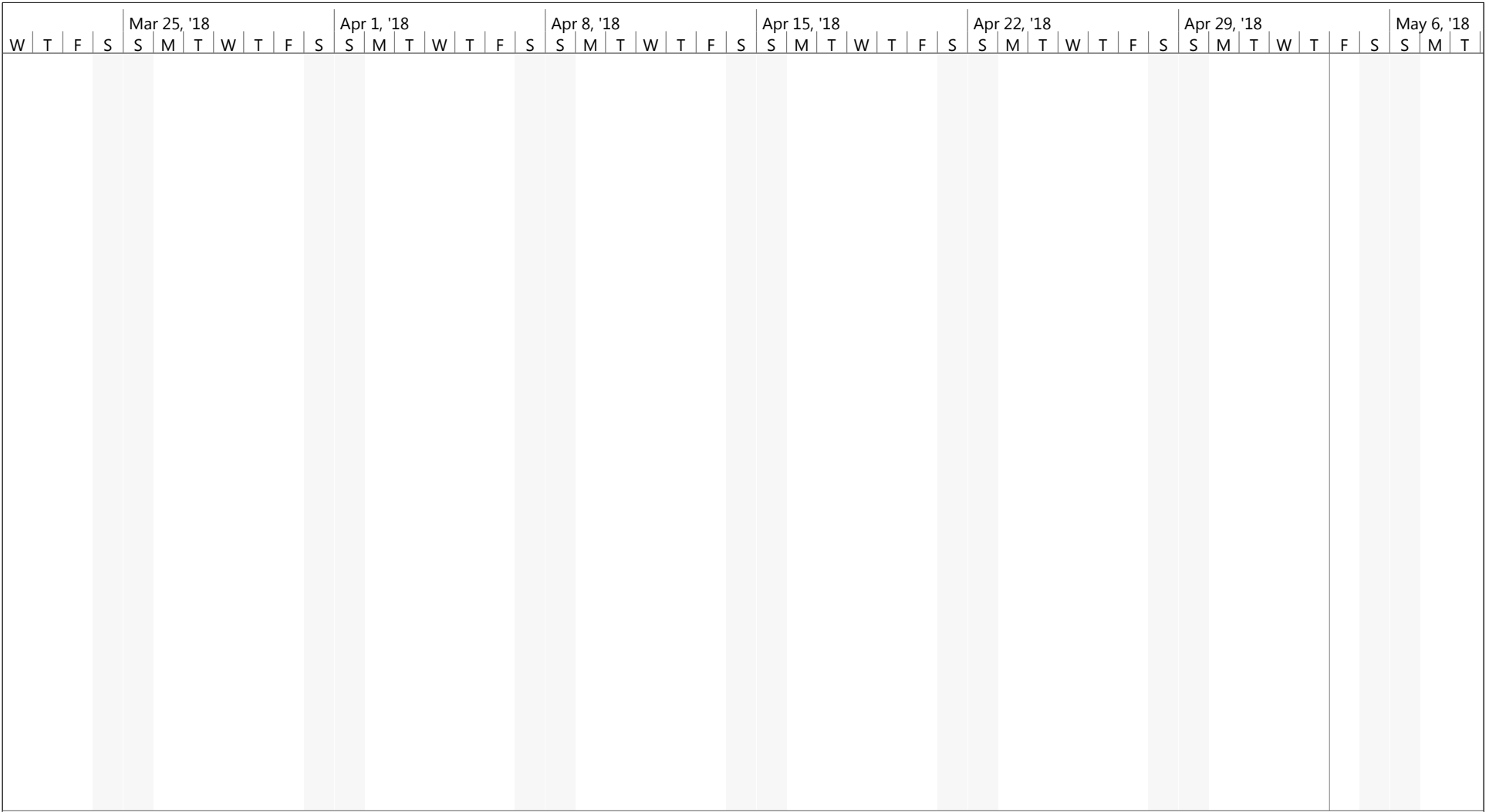
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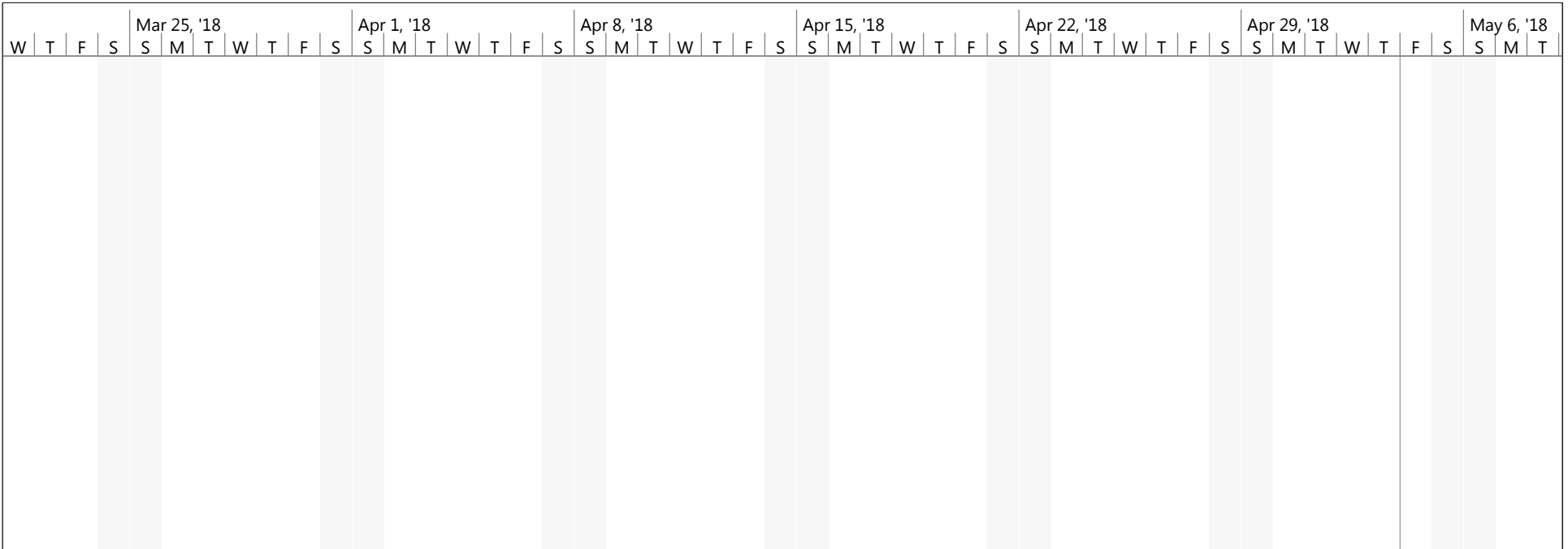


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